# INSTRUCTION BOOK 

for
FREQUENCY SHIFT CONVERTER

## BOEHME TYPE 5-C, SERIES B



MANUFACTURED BY
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## SAFETY NOTICE

Severe shock may result from careless handling when servicing this unit. Be careful not to contact high voltage or 115 volt input connections. When working inside the equipment, after the power has been turned off, always short-circuit the high voltage capacitors.

SECTION I
DESCRIPTION

1. GENERAL

The Boehme Frequency Shift Converter, Type 5-C, Series B, is designed to combine and convert the output of two diversity receivers into polar or neutral keyed D. C. The input may be either fre-
quency shift or make - break signals. The output can be used either directly or through relays to drive teletypewriters, ink recorders, or transmitters.

## 2. SPECIFICATIONS

Input Impedance
Input Signal Level
Input Frequencies

Keying Speeds

- 500 w. p.m. on make-break keying
- 250 w.p.m. on frequency shift keying

Output Level - . 065 amperes, neutral

- . 045 amperes, polar. Remains constant at any pre-set level. Will not reflect input variations until minimum input requirement is exceeded.

Output Ungrounded
Output Impedance

- 100 to 100,000 ohms

Power Source

- 115 volts, plus/minus 20\%
- 60 cycles, plus $20 \%$, minus $10 \%$
- 150 watts approximately

Tube Complement

| Number | Type | $\frac{\text { Reference }}{\text { Symbol }}$ | Number | Type | $\frac{\text { Reference }}{\text { Symbol }}$ |
| :---: | :---: | :---: | :---: | :---: | :---: |
| 4 | $6 J 6$ | VI | 4 | 6AL5 | V3 |
|  |  | V2 |  |  | V4 |
|  |  | V7 |  |  | V5 |
|  |  | v8 |  |  | v6 |

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| Number | Type | $\frac{\text { Reference }}{\text { Symbol }}$ | Number | Type | Reference Symbol |
| :---: | :---: | :---: | :---: | :---: | :---: |
| 4 | 6 Y 6 | v9 | 2 | 6AK6 | V14 |
|  |  | V10 |  |  |  |
|  |  | VII | 1 | 2AP1 | V15 |
|  |  | V12 |  |  |  |
|  |  |  | 2 | 5 Y 3 | v16 |
| 2 | 6AK6 | V13 |  |  | V17 |

## 3. CONSTRUCTION

The Frequency Shift Converter is rugged and compact in design, with the front panel carrying all controls necessary for its operation. The unit is designed for standard 19" relay rack mounting and the panel is $10 \mathrm{l} / 2^{\prime \prime}$ high.

It is provided with a dust cover which is removed by pulling straight back. The terminal strip and polar-neutral switch are mounted on the subpanel and are accessible when the dust cover is removed.
4. DIMENSIONS AND WEIGHTS
a. Maximum overall dimensions - 10 I/2" high, 19" wide, 15". deep
b. Packing dimensions - $31 \times 20 \times 26$
c. Net weight - 67 lbs .
d. Shipping weight - 157 lbs.

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SECTION II
INSTALLATION AND OPERATION
5. INSTALLATION
a. The Frequency Shift Converter is shipped from the factory with the input terminals connected to the 500 ohm windings of the imput transformers $\mathrm{T}-1$ and $\mathrm{T}-2$. If 5000 ohm input is required, unsolder the lead con lected to terminal \#2 of the input transformers and connect to terminal \#3. The tubes are packed separately and the tube sockets are plainly marked.
b. Fasten the unit to a standard 19" relay rack. A ten point terminal strip is mounted on the lower right hand side of the subpanel. To this terminal strip connect the incoming line from receiver \#l, the incoming line from receiver \#2, the power source and the line to the equipment being actuated and to a Boehme 6-E Tone Keyer if required. AIl connections are plainly marked.
c. The POLAR-NEUTRAL switch, Sw-7, located on the lower left hand side of the subpanel, should be switched to either polar or neutral, as required. For actuating a polarized relay, such as in a high speed ink recorder, polar output will give more positive operation. For actuating a non-polarized relay, such as in a teletypewriter, neutral output is required.
6. CONTROLS AND THEIR USES

Careless setting of the controls can cause damage to the equipment. For this reason, it is important to know the function of every control. The actual theory and operation is discussed in sections III and IV.

The controls on the front panel are shown in figure 1 and the functions are listed in the following table.

SYMBOL
A OUTPUT CURRENT

F FUSE
L-2 \& TUNING
I-4

R-18
SIGNAL BALANCE

R-34 INTENSITY

R-35 FOCUS

FUNCTION
Milliammeter. Indicates both current amplitude and polarity of output signal.

Line fuse. Protects the electrical system.
Variable inductance. Works in conjunction with SW-8 and tunes the selected range to match the frequency shift spread of the converter to the received signal.

Potentiometer. Corrects the unbalance between mark and space time relationship which may be present in the incoming signal.

Potentiometer. Controls brilliance of the oscilloscope pattern.

Potentiometer. Adjusts sharpness of the oscilloscope pattern.

## SYMBOL

R-50 NOISE REJECTOR

SW-I F.S.-A.M.

SW-2 CHANNEL 2 OUT - IN

SW-3 OUTPUT LEVEL

SW-4 OUTPUT REVERSE

SW-5 CH. $1-\mathrm{CH} .2$

SW-6 OFF - ON

SW-8 FREQUENCY SHIFT RANGE KC

V-15 TUNING INDICATOR

## FUNCTION

Potentiometer. Biases out the noise and permits signal which is greater than the noise to actuate the following circuits. It is used only with make-break signals.

Two position switch. Selects either frequency shift operation or make-break operation.

Toggle switch. Cuts channel 2 in or out of the circuit.

Four position switch. Controls the amount of resistance and capacitance in the output circuit.

Toggle switch. Reverses the flow of current through the output circuit in the event mark and space signals are inverted.

Toggle switch. Selects either channel 1 or 2 input for viewing on the oscilloscope.

Toggle switch. Removes or applies power to the unit.

Six position switch. Divides the total 200 -15,000 cycle frequency shift spread into six ranges.

Oscilloscope. Indicates the tuning of frequency shift signals. It is not used on make-break signals.
7. OPERATION
a. Referring to the Front Panel View (Fig. I), set the controls as follows:

Sw-5 - Oscilloscope switch to Channel 1.
SW-1 - Imput selector switch to FS.
R-I8 - SIGNAL BALANCE control to middle of its arc.
SW-2 - CHANNEL SWITCH 2 to OUT.
Sw-6 - POWER SWITCH to ON position. (Allow approximately thirty seconds for the tubes to heat up.)

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Figure 1. PRONT PAHEL VIEW

R-34 - INTENSITY control and
R-35 - FOCUS control
R-50 - NOISE REJECTOR control
to positions where the spot on the oscillo scope tuning indicator is small and sharply defined.
to its extreme counter-clockwise position. (NOTE: The noise rejection voltage is not utilized in frequency shift reception.)
b. Set Sw-8, the FREQUENCY SHIFT RANGE switch, to the range which includes the spread of the frequency shift signal. Tune the beat frequency oscillator of \#1 receiver until the low frequency signal forms a horizontal pattern of maximum amplitude on the TUNING INDICATOR oscilloscope.
c. Rotate the TUNING control L-2 and L-4 until a vertical pattern of maximum amplitude is formed. When these two patterns (horizontal and vertical) are ninety degrees out of phase with each other and have their maximum amplitudes, it indicates that the frequency spread of the frequency shift signal is matched with the filters of the Frequency Shift Converter.
d. Set oscilloscope switch Sw-5 to Channel 2. Tune the beat frequency oscillator of receiver \#2 until the oscilloscope TUNING INDICATOR shows two patterns (horizontal and vertical) ninety degrees out of phase with each other and having maximum amplitudes. The two receivers are now properly tuned and are diversity combined when channel switch Sw -2 is set to IN position.
e. By observing the terminating equipment for the best positive operation, the OUTPUT LEVEL switch Sw-3, the OUTPUT REVERSE switch Sw-4, the SIGNAL BALANCE control R-18 and the POLARNEUTRAL switch Sw- 7 may be adjusted.

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f. In the event make-break operation is desired, set switch Sw-l to AM and advance the NOISE REJECTOR control $\mathrm{R}-50$ until clean keying is observed on the terminating equipment. With make-break operation the OSCILLOSCOPE, FREQUENCY SHIFT RANGE switch and the TUNING control are not in service.
8. ABNORMAL OPERATING CONDITIONS
a. In make-break operation, high noise pulses may key the amplifier. To suppress line noise, turn NOISE REJECTOR in a clockwise direction.
NOTE: If NOISE REJECTOR R-50 is turned too far in a clockwise direction the signals also will be rejected. Adjust this control only to the point where the best performance of the receiving equipment is obtained.
b. Proper operation of teletypewriter equipment requires that the duration of the mark signal be approximately equal to that of the space signal. If the mark-to-space time relationship is unequal, the driven equipment may fail to operate. To increase the time duration of the mark signal in relation to the space signal, turn SIGNAL BALANCE potentiometer R18 in a clockwise direction.
c. In usual teletypewriter and telegraph practice, the mark signal is transmitted as one frequency and the space signal as a different frequency. If these conditions are reversed, the unit will produce an inverted output causing the mark signal to be positive with respect to the space signal. To correct this inversion, throw OUTPUT REVERSE switch SW-4 to the left-hand position.

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SECTION III
THEORY

## 9. BLOCK DIAGRAM (fig. 2)

The unit can be broken down into the following basic circuits.

| Stage | Symbol P | Par. |
| :---: | :---: | :---: |
| Input Amplifier - <br> Limiter Channel 1 | V-1 | 10 |
| Input Amplifier Channel 2 | V-2 |  |
| Filter | $\begin{array}{cc} \mathrm{Ll} \text { to } \mathrm{I}_{4} \\ \& & \mathrm{SW}-8 \end{array}$ |  |
| Discriminator | $\begin{aligned} & V-3, V-4, \\ & V-5, V-6 \end{aligned}$ | 11 |
| Limiter - Amplifier | V-7 | 12 |
| Low Pass Filter | $\mathrm{CH}-1$ \& C-6 |  |
| D.C. Amplifier \& Phase Inverter | V-8 | 13 |
| Signal Balance Control | R-18 |  |
| Output Section | $\begin{aligned} & \mathrm{V}-9, \mathrm{~V}-10, \\ & \mathrm{~V}-11, \mathrm{~V}-12 \end{aligned}$ | 14 |
| Oscilloscope | $\begin{gathered} \mathrm{V}-13, \mathrm{~V}-14_{4}, \\ \mathrm{~V}-15 \end{gathered}$ | 15 |
| Power Supplies | V-16, V-17 | 16 |

10. INPUT AMPLIFTER-LIMTTER AND FILTER

The Imput Amplifier-Limiter is composed of two identical circuits one for each receiver output. Since imput \#l is identical to input \#2, only input \#l will be analyzed.
amplified and limited by tube V-l. The plate currents of tube $\mathrm{V}-\mathrm{I}$ flow through the filter circuits and the compensating resistors $\mathrm{R}-5$ and $\mathrm{R}-6$. The filter circuit composed of L-工 and $\mathrm{C}-2$ is tuned to 1000 cycles and comprises the fixed frequency of the converter. The condensers $\mathrm{C}-3,16$, $18,20,22,24,26,28,30$ and 32 are arranged in six groups to make six overlapping ranges. The variable inductance L-2 connected to any one of these groups will tune through one of the six ranges. I-2 connected successively to these six condenser groups can be tuned through a total range of 1,200 to 16,000 cycles and comprises the variable frequency of the converter. Assuming $\mathrm{L}-2$ is tuned to 1850 cycles and the incoming tones of a frequency shift signal are 1000 and 1850 cycles respectively, the magnitude of the output signals applied to the primaries of transformers T-5 and T-6 will vary as the frequency of the signal shifts. The secondaries of T-5 and T-6 feed into the plates of two double diode rectifiers and since the two rectified signals are connected across resistors $\mathrm{R}-11$ and $\mathrm{R}-12$ in polarity opposition, discrimination of the two signals is accomplished.
b. Switch SW-I marked F.S. - A.M. selects either frequency shift or makebreak keying. For the latter type of operation, half of the input transformer secondaries TI and T2 are tied directly to the primaries of T6 and T8. The entire input section used in F.S. operation is thus shorted out.
c. Switch SW-2, marked channel 2, is utilized to switch channel 2 in or out of the circuit.


Pigure 2. Block Diagram

## 11. DISCRIMINATOR

a. The signal output from the amplifier-limiter is applied to the plates of the double diode rectifiers V-3, V-4 and the cathode load resistances $\mathrm{R}-11$ and $\mathrm{R}-12$. The extremes of R-11 and R-12 are connected across the grid and cathode of $V-7$. Since the rectified signals across R-ll and R-12 are in polarity opposition, the magnitude of the signal determines the conductivity of $\mathrm{V}-7$.
b. NOISE REJECTOR control R-50 is in the cathode circuit of $\mathrm{V}-4$ and $\mathrm{V}-6$ and supplies positive bias to the discriminator. Signal or noise will not appear at the grid of V-7 until the value of this bias voltage is exceeded. The control is not utilized in frequency shift operation.

## 12. LIMITER-AMPLIFIER STAGE AND LOW-PASS FILTER

Limiter-amplifier V-7 consists of a twin triode hooked in parallel with a low-pass filter in the plate circuit. The function of this stage is to limit the peaks of, and remove the ripple from, the rectified output pulses of discriminator rectifiers V-3 and V-4. The stage also suppresses short-duration high-level noise pulses and converts the rectified d-c pulses into trapezoidal wave signals.
a. Resistor R-15 is part of a bleeder network composed of resistors $\mathrm{R}-16, \mathrm{R}-17$ and variable resistor $\mathrm{R}-18$. This network is connected across the Bf supply and returns to ground through resistor $\mathrm{R}-49$. The grid of d-c amplifier V-8A is connected to the junction of resistors $\mathrm{R}-16$ and R-17. The sliding arm of potentiometer R-18 is connected to a source that is negative with respect to ground. The voltage division across the bleeder network is such that the bias voltage applied to the grid is
always negative with respect to ground or its cathode. The magnitude of this negative potential depends on the setting of SIGNAL BALANCE control R-18.
b. The mark signal developed across resistor R-1l as a result of full-wave rectification is applied to the grid of limiter-amplifier V-7. Since the input audio tones are keyed alternately, the signals consist of rectified d-c pulses containing a ripple frequency twice the frequency of the input tone signal. If the peak amplitude of the ripple reaches a sufficiently high potential, it will drive limiter-amplifier V-7 to cutoff. When V-7 is driven to cutoff, the plate current falls to zero and the plate potential rises to full plate supply voltage. As the mark signal ceases, the space signal drives the grid of V-7 positive. Plate current begins to flow and plate potential decreases. This limiting action of tube V-7 clips part of the ripple voltage from the mark signal. The remaining ripple is removed by the lowpass filter as described in subparagraph c below. When $\nabla-7$ is cutoff, the only current flowing through resistor R-15 is bleeder current. Under these conditions, the voltage at the junction of resistors $\mathrm{R}-16$ and $\mathrm{R}-17$ which is applied to the grid of d-c amplifier $\mathrm{V}-8 \mathrm{~A}$ rises.
c. The low-pass filter in the plate circuit of limiter-amplifier $\mathrm{V}-7$ consists of inductance CHI and $\mathrm{C}-6$. The filter removes the ripple from the output of limiter-amplifier tube V-7 (subparagraph (1) below), blocks short-duration high-level noise pulses (subparagraph (2) below), and changes the waveshape of the square-wave mark signals (subparagraph (3) below.)
(1) Inductance CHI presents a high impedance and the associated capacitor has a comparatively low impedance at the ripple frequency.

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Therefore the greater part of the ripple voltage appears across inductance CH and a much smaller portion appears across the capaoitor C-6. In actual operation, limiter-amplifier $V-7$ is driven with a relatively high-amplitude signal, which causes low-level clipping of the ripple frequency at the output of $\mathrm{V}-7$ (subparagraph (2) below.) Only high-frequency components of the ripple frequency appear across the filter network. This permits still better ripple attenuation, since the impedance of the filter increases with increasing frequency. The desired signal, which at this point is a low-frequency keyed d-c voltage, is attenuated relatively little by the filter. This signal appears across resistor $\mathrm{R}-15$, the load resistor of the filter section. The signal is applied to the grid of d-c amplifier V-8A through series resistors $\mathrm{R}-16$ and $\mathrm{R}-17$.
(2) High-amplitude, shortduration noise pulses will drive the grid of limiter-amplifier V-7 to cutoff for an instant, which ordinarily would cause a positive voltage to appear on the grid of d-c amplifier V-8A. However, inductance CHI opposes the decrease of plate current in limiteramplifier $\mathrm{V}-7$ and the capacitor discharges through resistor R-15. If limiter-amplifier $V-7$ is cutoff only for an instant, the current through resistor R-15 is maintained almost constant. In this way, short-duration noise pulses do not effect the operation of the succeeding stages.
(3) The low-pass filter will completely block frequencies above a certain critical cutoff frequency. Frequencies below cutoff frequency will be passed. However, the filter offers some opposition to these frequencies also and tends to remove
them. Therefore, the squarewave keying signals, whose frequency must be below cutoff frequency, also are affected by the filter. The inductance CHI opposes the sharp decrease in plate current caused by the sharp cutoff of limiter-amplifier V-7 at the beginning of a mark signal and also opposes the sharp increase in plate current at the beginning of the space signal. While the leading edge of the mark signal is rising the capacitor C-6 charges exponentially to a higher positive potential through resistor $\mathrm{R}-15$. As the leading edge of the space signal increases the capacitor discharges through resistor $\mathrm{R}-15$. The decay time of the capacitor is prolonged by inductance CHI . Thus both the leading and the trailing edge of the mark signal waveform are sloped.
13. DC AMPLIFIER STAGE, SIGNAL BALANCE CONTROL R-I8 AND PHASE INVERTER STAGE.

D-C amplifier V-8A consists of one section of a twin triode. D-C phase inverter $\mathrm{V}-8 \mathrm{~B}$ is the other section of the triode. The function of the d-c amplifier is to amplify the keyed signals from limiter-amplifier V-7. SIGNAL BALANCE control R-18 establishes the correct mark-to-space time ratio. D-C phase inverter V-8B inverts the output of d-c amplifier V-8A and provides keyed signals of the correct polarity to the output amplifiers.

> a. D-C amplifier V-8A.

This circuit is similar to that of V-7.
$\mathrm{V}-8 \mathrm{~A}$ is keyed by a voltage obtained from the junction of resistors R-16 and R-17. This junction is positiv, with respect to ground during mark conditions and negative during space conditions. The plate circuit of V-8A

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is a resistor network consisting of $\mathrm{R}-20$, R-24 and R-25. Voltage distribution of the network is such that the junction of $\mathrm{R}-24$ and $\mathrm{R}-25$ is negative during mark and positive during space conditions. This junction feeds the grid of phase inverter $\mathrm{V}-8 \mathrm{~B}$ as well as the grid of either V-9 or V-10 depending on the setting of the output reverse switch.
potential and the mark signal is not positive enough to overcome the bias until near the end of its leading edge. The same action occurs at the end of the mark signal. If the bias on the grid is small, the tube does not return to cutoff condition until near the end of the trailing edge of the mark signal waveshape. If the bias is great, cutoff occurs near the be-


A

$$
T_{1}>T_{2}
$$

$T_{1}<T_{2}$

SIGNAL BALANCE CONTROL R18 SET at extreme clockwise position.


SIGNAL BALANGE GONTROL CORRECTLY SET NEAR MIDDLE OF ITS RANGE.

$$
T_{1} \cong T_{2}
$$

Figure 3. Function of SIGNAL BALANCE control R18.
b. SIGNAL BALANCE Control R-18. The purpose of SIGNAL BALANCE control R-18 is to adjust the mark-to-space time ratio of the keyed signal. Depending on the setting of the potentiometer, the static bias on the grid of d-c amplifier V-8A is negative. The positive mark signal from limiter-amplifier V-7 first must overcome this bias before d-c amplifier V-8A can conduct. If the control is set at or near its maximum counter-clockwise position, the bias on the grid is relatively small and d-e amplifier V-8A will conduct toward the beginning of the leading edge of the mark signal waveshape. If the control is set at or near its maximum clockwise position, the bias is far below cutoff
ginning of the trailing edge. D-c amplifier V-8A therefore conducts for
a greater or lesser portion of the mark signal from limiter-amplifier $\mathrm{V}-7$ depending on the setting of SIGNAL BALANCE control R-18. Hence the width of the mark signal is directly proportional to the time interval between plate current on and plate current off of $d-c$ amplifier V-8A and also is inversely proportional to the width of the space signal.

## c. D-C Phase Inverter $V-8 B$.

(1) Space signal. The grid of d-c phase inverter $V-8 B$ is directly connected to the junction of resistors R-24 and R-25. During a space signal,

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the voltage at the junction of these resistors is positive. With a positive voltage applied to its grid, the d-c phase inverter conducts. Plate current flows through resistor R-2l, which is part of a bleeder network in the power supply. This bleeder network consists of resistors $\mathrm{R}-22, \mathrm{R}-23$ and $\mathrm{R}-21$. When the $\mathrm{d}-\mathrm{c}$ phase inverter conducts, both plate and bleeder current flow through resistor R-2l. Under this condition, the voltage at the junction of resistors $\mathrm{R}-22$ and $\mathrm{R}-23$ is negative. Depending on the setting of output reverse switch $\mathrm{SW}-4$, this voltage is applied to the grid of either V-9 or V-10.
(2) Mark signal. During a mark signal, the voltage at the junction of resistors $\mathrm{R}-24$ and $\mathrm{R}-25$, which is the voltage applied to the grid of the d-c phase inverter, is negative. This voltage is sufficient to cut off the phase inverter, and plate current ceases to flow. The only current that flows through resistor R-2l is bleeder current and the voltage at the junction of resistors $\mathrm{R}-22$ and $\mathrm{R}-23$ rises.
14. THE OUTPUT SECTION

The output section comprises tubes V-9, V-10, V-11, V-12, output level switch Sw-3, output reverse switch Sw-4, polar-neutral switch Sw-7 and associated components.
a. During space conditions, when the grid of V -lo is negative and the grid of V-9 is positive, current flows from B plus through tube V-12, resistor $\mathrm{R}-29$ and tube $\mathrm{V}-9$. In a parallel path current also flows from B plus through tube V-11, the output load circuit, resistor $\mathrm{R}-29$ and tube V-9. Since the grid of V-11 is at the same potential as its cathode, the internal resistance of the tube will be low and losses incurred
will be minimized. At the same time the grid of V-l2, by virtue of the load current flowing through R-29, is biased negatively, thus causing very little flow of current through tube $\mathrm{V}-12$ and minimizing the loss in the parallel path. During marking conditions the grid of V-9 is negative and the grid of V -10 is positive. In this condition current flows through the load circuit in the opposite direction and the functions described above reverse.
b. The switch Sw-3 is the output level switch which limits the flow of current through the load circuit and injects suitable output shunt capacitance on each tap except \#4. Tap \#4 provides pulse sharpening which not only steepens the wave front, but increases the current to the output circuit for a very short pulse. The effect of this pulse is not shown on the output meter since the time duration is so short. It will, however, produce a marked effect on any terminating equipment since it will drive the mechanisms more abruptly. The peak value of this pulse is shown on the output load current curve (fig. 4). Pulse sharpening is not employed on neutral outputs and tap \#4 of switch Sw-3 is readable on the output meter.
c. When switch $\mathrm{Sw}-7$ is set for neutral operation, tube V-12 has no $B$ plus and is out of the circuit. Current will flow through the output load only when the grid of V -lo is negative and that of V-9 positive. The path will be from B plus, through V-1l, the output load, switch Sw-3, $\mathrm{R}-29$ and V-9. When the grid of V-9 becomes negative and that of $\mathrm{V}-10$ positive there will be no current flow through the load. However, there will be some flow through V-11, $\mathrm{R}-30$ and $\mathrm{V}-10$. This will be limited since sufficient negative voltage is developed across $\mathrm{R}-30$ to bias the grid of V-11.
d. The OUTPUT REVERSE switch Sw- 4 reverses the operation of the output circuit so that mark and space signals may be inverted.
15. THE OSCILLOSCOPE SECTION

The Oscilloscope Section comprises tubes V-13, V-I4, V-15, oscilloscope switch Sw-5, potentiometers R-34 and $\mathrm{R}-35$ and associated components.

The oscilloscope is provided with vertical and horizontal amplifiers V-13 and V-14. The signal to the amplifiers is controlled by switch Sw-5. This switch selects either Channel 1 or Channel 2 for viewing on the oscilloscope.

Potentiometer R-35 controls the focus and R-34 controls the intensity of the oscilloscope tube.

Switch Sw-l of the Audio Section removes the oscilloscope from the circuit when make-break keying is employed.
16. POWER SUPPLY

The Power Supply Section comprises transformers T-3 and T-4, tubes V-16 and V-17, chokes $\mathrm{Ch}-2$ and $\mathrm{Ch}-3$ and associated components.

The power supply consists of one full wave rectifier which supplies current to all of the plate circuits, and one half wave rectifier which supplies current to the D.C. amplifiers and to the oscilloscope tube. The same transformer winding is used for both power supplies.

The heater transformer T-4 supplies voltage to all the tubes in the unit.

## 17. INSPECTION

a. Periodic inspection should be made of the Frequency Shift Converter by examining the cords and connections to the associated equipment. Be sure all connections are secure.
b. Before removing the dust cover, be sure the power is of $f$. Remove any dust that may have collected. taking care not to damage the wiring.
18. LOCATING TROUBLE Refer to the schematic wiring diagram (fig. 8).
a. Check fuse.
b. Check vacuum tubes.
c. In the event the fuse and the vacuum tubes are in good condition, refer to the point to point voltage table and check in the following sequence:

1. Failure of the tubes to light indicates that the transformer T-4 or switch Sw-6 may be faulty. Determine the location of the fault by using an A.C. voltmeter and checking voltage points in that particular circuit.
2. Check the power supply output voltages by measuring from ground to R-48 and from ground to R-5l. If these voltages correspond with those set forth in the voltage table, the power supply section is in working order.
3. The fault may be isolated more readily by checking the voltage
between the grid and cathode of V-7. This is the rectified signal voltage and should it not conform with the voltages set forth in the voltage table, the trouble will be found in the audio section. If the voltages appearing across the grid and cathode of V-7 are correct, comnect the positive prod of the voltmeter to ground and the negative prod to the grid of V-9. Should the voltages indicated on the meter not correspond with the voltages set forth in the table, the fault will lie in the D.C. converter section. If the voltages appearing at the grid of V-9 are correct, the fault will lie in the output section. After the approximate location of the fault has been determined, check the voltages appearing in the various branches of that particular circuit. The oscilloscope is independent of the operation of the audio section, the D.C. converter section and the output section; hence, any failure of the oscilloscope tube to indicate correctly will be found in that circuit.
4. REPLACEMENT OF PARTS
a. The Frequency Shift Converter is so constructed that all parts are accessible either on the subpanel or behind the front panel. Electrical connections are all within easy access.
b. Care should be taken in soldering or unsoldering connections that solder does not drip and short out any part.
5. VOLTAGE TABLE
a. Voltages are measured plus/

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| VOLTAGE TABLE |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| D. C. VOLTS SEE PAR 20 FOR CONTROL SETTINGS AND INSTRUCTIONS |  |  |  |  |  |  |  |  |  |
|  | A C | C E | M A R K |  |  | SYMBOL | POINT | SYMBOL | POINT |
|  |  | 175 | 175 | Measu | across | R-49 |  |  |  |
|  |  | 210 | 210 | " | , | R-48 |  |  |  |
|  |  | 240 | 240 | " | " | R-51 |  |  |  |
|  |  | 75 | 75 | " | " | R-50 |  |  |  |
|  |  | 385 | 385 | " | between | Ground | BL | R-48 | 0 |
|  |  | 315 | 315 | " | " | " | BL | R-51 | GY |
|  |  | 200 | 200 | " | " | " | BL | V-1 | Pin \#1 |
|  |  | 200 | 200 | " | " | " | BL | V-I | " \#2 |
|  |  | 200 | 200 | " | " | " | BL | $\mathrm{V}-2$ | " \# |
|  |  | 200 | 200 | " | " | " | BL | $\mathrm{V}-2$ | " \#2 |
|  |  | 35 | 180 | " | " | " | BL | V-7 | " \#1 |
| Less | than 0 | 0.5 | 13 | " | " | " | BL | V-7 | " \#6 |
|  |  | 205 | 22 | " | " | " | BL | V-8 | " \#1 |
|  |  | 40 | 210 | " | " | " | BL | V-8 | " \#2 |
| Less | than | 1 | 75 | " | " | " | BL | V-8 | " \#5 |
|  |  | 20 | Less than 1 | " | " | " | BL | $\mathrm{V}-8$ | " \#6 |
|  |  | 285 | 285 | " | " | " | BL | V-15 | " \#2 |
|  |  | 320 | 320 | " | " | " | BL | V-15 | " \#3 |
|  | 110-2 | -220 | 110-220 | " | " | " | BL | V-15 | " \#4 |
|  |  | 320 | 320 | " | " | " | BL | V-15 | " \#6 |
|  |  | 320 | 320 | " | " | " | BL | V-15 | " \#7 |
|  | 240-3 | -370 | 240-370 | " | " | " | BL | V-15 | " \#10 |
|  |  | 130 | 130 | " | " | " | BL | V-13 | " \#5 |
|  |  | 180 | 180 | " | " | " | BL | V-13 | " \#6 |
|  |  | 11 | 11 | " | " | " | BL | V-13 |  |
|  |  | 130 | 130 | " | " | " | BL | V-14 | " \#5 |
|  |  | 180 | 180 | " | " | " | BL | V-14 | " \#6 |
|  |  | 17 | 11 | 11 | " | " | BL | V-114 | " \#7 |

NOTE: Mark and space voltages may be inverted beyond this point but can be corrected with the output reversing switch.

| Less than | 25 | 275 | Measure | between | Ground | BL | V-9 | Pin \#3 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | 1 | 75 | , | " | " | BL | V-9 | " \#5 |
|  | 270 | 25 | " | " | " | BL | V-10 | \#3 |
|  | 70 Less than | 1 | " | " | " | BL | V-10 | " \#5 |
| Less than | 15 | 225 | " | " | V-11 | Pin\# | $8 \mathrm{~V}-11$ | " \#3 |
|  | 1 | 42 | " | " | V-11 |  | $8 \mathrm{~V}-11$ | " \#5 |
|  | 225 | 15 | " | " | V-12 |  | $8 \mathrm{~V}-12$ | " \#3 |
|  | 42 Less than | 1 | " | " | V-12 |  | $8 \mathrm{~V}-12$ | \#5 |

minus 20\%. The D.C. voltages should be checked on a 20,000 ohm per volt meter.
b. All readings are to be taken with the Frequency Shift Converter set as follows:

Refer to the Front Panel View (fig. 1).

Set control switch Sw-5 to Channel 1 .

Set switch Sw-I to FS.
Set control R-50 to its extreme counter-clockwise position.

Set control R-18 to the middle of its arc.

Set control Sw-3 to \#l position.
Set switch Sw-6 to ON position.
Set switch Sw-2 to OUT position.
Set polar-neutral switch to
POLAR.

Attach a 2000 ohm 5 watt load resistor to the output terminals.

Attach a tone source to the \#l input terminals.

When reading marking voltages, set the tone source to 1000 cycles and the output level to at least zero db. Adjust the oscilloscope controls R-34 and $\mathrm{R}-35$ until a clear pattern is observed. The pattern on the oscilloscope screen should be on the horizontal plane and at maximum amplitude. In the event the pattern is not tuned to maximum, adjust the fixed tuned element to Channel 1 until maximum amplitude is observed with 1000 cycle input. When reading spacing voltages, adjust the input frequency to 1850 cycles and rotate the tuni.ng control until maximum vertical pattern is obtained. When checking voltages of Channel 2 input, connect the tone source to the Channel 2 input terminals and measure voltages in that particular circuit.

| V Section | FREQUENCY SHIFT CONVERTER |  | Component | Parts List |
| :---: | :---: | :---: | :---: | :---: |
|  | Boehme Type 5-C, Series |  |  |  |
|  | SECTION V |  |  |  |
|  |  |  |  | BOEHME |
| S Y M B O L | DESCRIPTION | QUAN. | MIL TYPE | PART NO. |
|  | Tuning Unit Assembly; Capacitor $\mathrm{C}-2$ or $\mathrm{C}-4$ and Inductance $\mathrm{I}-1$ or $\ln 3$ are part of this assembly | 2 |  | 5-C-C |
|  | Tuning Unit Assembly; Capacitor $\mathrm{C}-3$ or $\mathrm{C}-5$ and Inductance I-2 or L-4 are part of this assembly | 2 |  | S-C-D |
| A | Miliiammeter; WESTON 100-0-100 Bakelite case \#506 | 1 |  | $5-\mathrm{c}-40$ |
| C-1 | Capacitor; $25-25 \mathrm{mfd} ., 150 \mathrm{~V}$ 。 | 1 | CE32A250J | 5-c-265 |
| C-2,4,7,15 | Capacitor; $0.25 \mathrm{mfd} ., 600 \mathrm{~V}$. | 4 | CP53BIEF254K | 5-C-60 |
| C-3,5,9,10 | Capacitor; . $1 \mathrm{mfd} ., 600 \mathrm{~V}$. | 4 | CP27AlEFIOLK | 5-c-266 |
| C-6 | Capacitor; .1 mfd.,600V. | 1 | CP53B1EFIO4K | 5-C-64 |
| C-8 | Capacitor; 2.0 mfd.,600V. | 1 | CP53B1EF205K | 5-c-63 |
| C-11,12,32,33 | Capacitor; . $002 \mathrm{mfd} ., 500 \mathrm{~V}$. | 4 | CM30A202J | 5-C-267 |
| C-13,34,35 | Capacitor; $25 \mathrm{mfd},. 400 \mathrm{~V}$. | 3 | CE31A250Q | 5-C-264 |
| C-14 | ```Capacitor; 10-10-10-mfd., 400 V.``` | 1 | CE33A100Q | 5-c-263 |
| C-16,17,18,19 | Capacitor; . $05 \mathrm{mfd} ., 600 \mathrm{~V}$. | 4 | CP27AlEF503K | 5-C-268 |
| C-20,21,22,23 | Capacitor; . 01 mfd .9600 V . | 4 | CP27AlEFl03M | 5-C-269 |
| C-24,25 | Capacitor; . $02 \mathrm{mfd} ., 600 \mathrm{~V}$. | 2 | CP27AlEF203K | 5-C-270 |
| $\begin{gathered} \mathrm{C}-26,27,28,29, \\ 30,31 \end{gathered}$ | Capacitor; . $006 \mathrm{mfd} ., 600 \mathrm{~V}$. | 6 | CP27ALEF602M | 5-C-271 |
| Ch-1 | Choke; | 1 |  | 5-C-256 |
| Ch-2 | Choke $12 \mathrm{H}, 125 \mathrm{ma}$. | 1 |  | 5-C-250 |
| Ch-3 | Choke; $30 \mathrm{H}, 40 \mathrm{ma}$. | 1 |  | 5-C-251 |
| F | Fuse; $5 \mathrm{amp} ., 3$ AG. | 1 |  | 5-C-248 |
| L-1,2,3,4, | Coil | 4 |  | 5-C-EA |
| R-1,2,3,4 | Resistor; 1 meg., I/2 W. | 4 | RC20BF105K | 5-C-47 |
| $\begin{aligned} & \mathrm{R}-5,6,7,8,15, \\ & 20,21,41 \end{aligned}$ | Resistor; 47,000 ohm, 2 W. | 8 | RC4 18 FL 473 K | 5-C-42 |
| $\begin{gathered} \text { R-9,10,11,12, } \\ 13,16,22,23, \end{gathered}$ |  |  |  |  |
| $\mathrm{R}-14,43$ | Resistor; $470 \mathrm{ohm}, 1 \mathrm{~W}$. | 2 | RC30BF471K | 5-C-44 |
| R-17 | Resistor; 150,000 ohm, 1 W . | 1 | RC30BFI54K | 5-C-244 |


| S Y M B O L | DESCRIPTION | QUAN. | MIL TYPE | BOEHME <br> PART NO. |
| :---: | :---: | :---: | :---: | :---: |
| R-18,35 | Potentiometer; MallorylQM-70-MP 70,000 ohm, 4 W. | 2 |  | 5-c-56 |
| $\mathrm{R}-29,30,31$ | Resistor; 2000 ohm, 12 W . | 3 | RW32G202 | 5-C-288 |
| R-32 | Resistor; $3100 \mathrm{ohm}, 12 \mathrm{~W}$. | 1 | RW32G312 | 5-C-289 |
| R-33 | Resistor; 15,000 ohm, 2 W . | 1 | RC4IBF153K | 5-C-48 |
| R-34 | Potentiometer; MALLORY QM-50-MP 50,000 ohm, 4 W . | 1 |  | 5-C-57 |
| R-36 | Resistor; 33,000 ohm, 1 W . | 1 | RC30BF333K | 5-c-49 |
| R-37,53 | Resistor; 220,000 ohm, 1 W . | 2 | RC30BF224K | 5-C-50 |
| R-40,42 | Resistor; 22,000 ohm, 2 W. | 2 | RC41BF223K | 5-C-51 |
| R-44 | Resistor; 15,000 ohm, $\frac{1}{2} \mathrm{~W}$. | 1 | RC20BFI53K | 5-C-89 |
| R-45,46 | Resistor; 47,000 ohm, $\frac{1}{2}$ W。 | 2 | RC20BF473K | 5-c-53 |
| R-47 | Resistor; 4,700 ohm, $\frac{1}{2} \mathrm{~W}$. | 1 | RC20BF472K | 5-C-90 |
| R-48,49 | Resistor; 10,000 ohm, 60 W . | 2 | RW36G103 | 5-c-261 |
| R-50 | Potentiometer; 5,000 ohm, 25 W. | 1 | RP101FE502KK | 5-C-58 |
| R-51 | Resistor; 16,000 ohm, 60 W. | 1 | RW36G163 | 5-C-262 |
| R-52 | Resistor; $800 \mathrm{ohm}, 8 \mathrm{~W}$. | 1 | RW31G801 | 5-C-245 |
| Sw-1 | 4-circuit, 2-position switch; MALLORY 1312L | 1 |  | 5-C-273 |
| Sw-2,4,5,6,7 | ```Toggle Switch; D.P.D.T., 5A, l25 V.``` | 5 | ST22N | 5-C-272 |
| Sw-3 | 2-circuit, 5-position switch MALLORY 1315L | 1 |  | 5-C-274 |
| Sw-8 | 2-circuit, 9-position switch MALLORY 3229J | 1 |  | 5-C-275 |
| T-1,2 | Transformer; input | 2 |  | 5-C-253 |
| T-3 | Transformer; power | 1 |  | 5-C-252 |
| T-4 | Transformer; filament | 1 |  | 5-C-255 |
| T-5,6,7,8 | Transformer; interstage | 4 |  | 5-C-254 |
| V-I,2,7,8 | Tube, 656 | 4 |  | 5-C-80 |
| V-3,4,5,6 | Tube, 6AL5 | 4 |  | 5-c-81 |
| V-9,10,11,12 | Tube, 6Y6G | 4 |  | 5-C-82 |
| $\mathrm{V}-13,14$ | Tube, 6ak6 | 2 |  | 5-c-83 |
| V-15 | Cathode Ray Tube; 2 API-A | 1 |  | 5-C-257 |
| V-16,17 | Tube, 5Y3GT | 2 |  | 5-C-85 |



Pigure 4. Output Level Curve



$$
5-C-L
$$


$5-C-P$


## NOTES:

I. LI,L2,L3 \& L 4 ARE IDENTICAL-BOEHME PART NO. 5-C-EA
2. CAPACITORS C 2 \& C 4 ARE IDENTICAL-MIL TYPE CP53BIEF254K CAPACITORS C $3 \ddagger C 5$ ARE IDENTICAL-MIL TYPE CP27AIEFIO4K

Figure 7. Iuning Onit Assemblies


> H. O. BOEHME, INC.

COMBINED PARTS AND SPARE PARTS LIST




H. O. BOEHNE, INC.

COMBINED PARTS AND SPARE PARTS LIST


